

Dr. Durga Malladi
EVP and GM, Technology Planning, Edge
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Dr. Durga Malladi is Executive Vice President and General Manager, Technology Planning, Edge Solutions & Data Center, at Qualcomm Technologies, Inc. He joined as a Senior Engineer in 1998.

He is responsible for Technology product management and IP roadmap planning across all businesses in Qualcomm Technologies. This spans Artificial Intelligence (hardware, software, tools, hub), Processors (CPU, GPU, NPU), Connectivity (5G, 6G, Wi-Fi, Bluetooth, satellite communications, positioning), Data Center (general purpose compute, AI head node, inference & finetuning), Servers, Multimedia (computer vision, audio, video, sensors), Software, Developer Ecosystem, 5G-6G Infrastructure and Network Management, and Data Analytics. In addition, he is responsible for Qualcomm's infrastructure and network management solutions.

In prior roles, Durga led wireless research in Qualcomm from 4G to 5G and was head of systems department in Qualcomm Research. His responsibilities spanned system design, standardization, prototype testbeds, pre-commercial tests and trials.

Durga is an IEEE Fellow and holds 851 USPTO granted patents. He is a recipient of Distinguished Alumnus Award from Indian Institute of Technology, Madras, Qualcomm's IP Excellence Award, Distinguished Contributor Award for Project Leadership and Achievement Awards for Outstanding Contributions. He serves on the CTIA Board of Directors and the AI Governance Alliance Steering Committee at the World Economic Forum.

Durga holds a B.Tech. ('93) from Indian Institute of Technology, Madras, M.S ('95) and Ph.D. ('98) from UCLA, and an AI Graduate Certificate from Stanford ('23). His Ph.D. dissertation is on adaptive estimation and filtering techniques, and his research interests include artificial intelligence, signal processing, communication theory, theoretical computer science, and quantum computing.

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